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		Subclass	ISSUE CLASSIFICATION
		Class	

PATENT NUMBER

U.S. UTILITY Patent Application

Q.I.P.E.

PATENT DATE

SCANNED TUES 4 Q.A.

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/802108	D	709	24	2151	J. SAWYER

APPlicants

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TITLE

Computer Processing architecture having a scalable number of processing Paths and Pipelines

PTO-2040
12/99

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ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				Amount Due	Date Paid
	(Assistant Examiner)	(Date)	(Primary Examiner)	(Date)	
	(Legal Instruments Examiner)	(Date)		ISSUE BATCH NUMBER	

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